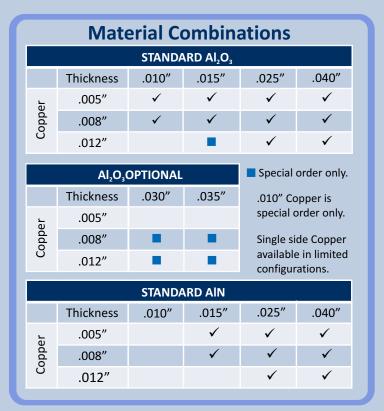
DIRECT BOND COPPER SUBSTRATES

Specifications and Design Rules



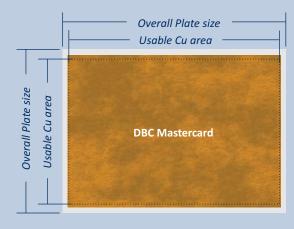
Physical Properties

Thermal conductivity:	Al₂O₃ 24-26 W/mK @ 20°C AlN 170-180 W/mK @ 20°C
Electrical resistivity:	>10 ¹⁴ Ωcm
Dielectric constant:	Al ₂ O ₃ 9.8 <u>+</u> 10% @ 1MHz 10.0 <u>+</u> 10% @ 1GHz AIN 9.0 <u>+</u> 10% @ 1MHz
Dielectric loss:	Al₂O₃ 0.0003 @ 23°C, 1MHz AIN 0.0005 @ 23°C, 1MHz
Dielectric strength (DC):	Al₂O₃ 20 kV/mm @ .025" 28 kV/mm @ .010" AIN 20 kV/mm @ .025"
Copper peeling strength: (typical Cu-thickness .008")	Al₂O₃ ≥22.8 lb/in. @ 2 in./min AlN ≥28.6 lb/in. @ 2 in./min
Operating temperature:	-55°C to +850°C (critical in H ₂ atmosphere >400°C)
E-Module (blank ceramic):	Al₂O₃ 340 GPa AIN 320 GPa
Electrical conductivity: (Cu surface)	58 x 10 ⁶ S/m



Geometric Properties

Mastercard Overall Plate size:	Nominal5.5" x 7.5"Actual5.43" x 7.50"		
Maximum usable Cu area:	5.0" x 7.0"		
Overall thickness tolerance of ceramic + copper:	+7%/-10%		
Ceramic perimeter tolerance:	Al_2O_3 as fired <u>+</u> 2% AIN as fired <u>+</u> 0.5%		
Copper surface finish:	$R_{max} \le .002''; R_a \le .00008'';$ $R_z \le .00063''$		



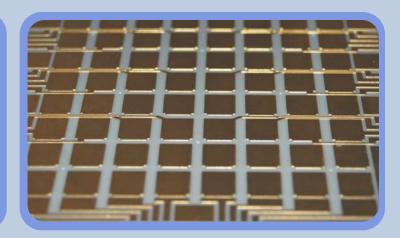


Physical Properties Cont'd

Bending Strength:
(blank ceramic)

Coefficient of linear thermal expansion for bare Cu: (dependent on Cu thickness) $AI_2O_3 > 350 \text{ N/mm}^2$ AIN > 300 N/mm²

6.8 ppm/K @ 20°C - 300°C Al₂O₃ 7.3 ppm/K @ 20°C - 600°C 8.0 ppm/K @ 20°C - 1000°C 4.7 ppm/K @ 20°C - 300°C AlN 5.2 ppm/K @ 20°C - 600°C 5.6 ppm/K @ 20°C - 1000°C



Design Dules								
Design Rules								
	Min. Width		Connor thickness	Minimum Ditah				
	Min. Space Typical .012"		Copper thickness @<.005"	Minimum Pitch024"				
Mininum width of Copper lines	Minimum .010"	· · · · · · · · · · · · · · · · · · ·		.020″				
Minimum width of spaces Typical .020" Minimum .016"				.040″				
			@ <u><</u> .008″	.032″				
Tighter tolerances are possible but	Typical .0275"		@<.010"	.056″				
not guaranteed in volume production.	Minimum .020"		@ <u><</u> .010" .040"					
	Typical .0275"		@ <u><</u> .012"	.056"				
	Minimum .020"		@ <u><</u> .012"	.040"				
Copper pull back: (lasered edge) typical P > .020" <u>+</u> .012" minimum P = .014" <u>+</u> .010" depends on copper thickness	Copper Ceramic Copper P = Copper pull back			rance: E 8"/002" @ ≤ .025" 2"/002" @ > .025" Copper Ceramic				
Front to back alignment: $A = \pm .008''$ Etching tolerance: $typical \pm .006'' @ \le .008'' Cu$ $minimum \pm .004'' @ \le .008 Cu$ $typical \pm .008'' @ \le .012'' Cu$ minimum $\pm .006'' @ \le .012 Cu$			 Chips along ceramic edge: Length of chip: maximum 1 x ceramic thickness Width of chip: ½ x ceramic thickness Surface plating (all over): Electroless Nickel 80µ" - 400µ" (8%±2% Phosphorus) Pd 2µ" - 12µ" Immersion Au 0.4µ" - 2µ" Electroless Au 2µ" - 50µ" Bonding Area (wetting): ≥90% 					
Taper of etched pattern: Maximum ½ Cu thickness	← max ½ T Copper T Ceramic t		Wire bonding: Standard Copper surface suitable for Stitch bond <8 mil Al, Au, Cu wire, > 8 mil wire requires a ball bond. no lift off allowed during pull test @ 10 mil Al wire					
All Copper feature measurements are taken from ceramic and copper interface unless otherwise noted.	er Copper Ceramic		10 mil Copper Ceramic	Copper Ceramic Allowed				

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